

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

5 Listing of Claims:

Claims 1-19 (canceled)

Claim 20 (currently amended): A liquid crystal display comprising:

- a lower substrate;
- 10 an upper substrate positioned parallel with the lower substrate; and
- a plurality of pixel units, each of the pixel units including an upper transparent electrode, a liquid crystal layer, a lower transparent electrode, and a color filter formed on the upper substrate and above the lower substrate;
- 15 wherein a surface of each color filter has a plurality of convex structures, and the upper transparent electrode directly contacts and covers the convex structures on the surface of each of the color filters.

Claim 21 (canceled)

- 20 Claim 22 (previously presented): The liquid crystal display of claim 20 wherein the surface of each color filter has the plurality of convex structures is able to scatter light.

- Claim 23 (previously presented): The liquid crystal display of claim 20 wherein a distribution density of the convex structures is used to regulate brightness and a color
- 25 deepness of the liquid crystal display.

- Claim 24 (previously presented): The liquid crystal display of claim 20 wherein each of the pixel units respectively comprises a reflection layer positioned between the color filter and the lower substrate.

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- Claim 25 (previously presented): The liquid crystal display of claim 24 being a reflective liquid crystal display.

Claim 26 (previously presented): The liquid crystal display of claim 24 wherein each of the reflection layers includes an opening.

- 5 Claim 27 (previously presented): The liquid crystal display of claim 26 being a semi-transmissive and semi-reflective liquid crystal display.

Claim 28 (previously presented): The liquid crystal display of claim 20 further comprising a plurality of thin film transistors for respectively controlling each of the
10 pixel units.

Claim 29 (previously presented): The liquid crystal display of claim 28 wherein the thin film transistors is formed on the lower substrate and below the upper substrate.

- 15 Claim 30 (new): The liquid crystal display of claim 20 wherein a surface each of the upper transparent electrodes opposite to the color filters is planar so that the liquid crystal display has a uniform cell gap between the upper substrate and the lower substrate.

- 20 Claim 31 (new): A liquid crystal display comprising:
a lower substrate;
an upper substrate positioned parallel with the lower substrate; and
a plurality of pixel units, each of the pixel units including an upper transparent electrode, a liquid crystal layer, a lower transparent electrode, and a color filter formed
25 on the upper substrate and above the lower substrate;
wherein a surface of each color filter has a plurality of convex structures, and each of the pixel units respectively comprises a reflection layer positioned between the color filter and the lower substrate.

- 30 Claim 32 (new): The liquid crystal display of claim 31 wherein the color filter is covered with the upper transparent electrode directly.

Claim 33 (new): The liquid crystal display of claim 31 wherein the surface of each color filter has the plurality of convex structures is able to scatter light.

5 Claim 34 (new): The liquid crystal display of claim 31 wherein a distribution density of the convex structures is used to regulate brightness and a color deepness of the liquid crystal display.

10 Claim 35 (new): The liquid crystal display of claim 31 being a reflective liquid crystal display.

Claim 36 (new): The liquid crystal display of claim 31 wherein each of the reflection layers includes an opening.

15 Claim 37 (new): The liquid crystal display of claim 36 being a semi-transmissive and semi-reflective liquid crystal display.

Claim 38 (new): The liquid crystal display of claim 31 further comprising a plurality of thin film transistors for respectively controlling each of the pixel units.

20 Claim 39 (new): The liquid crystal display of claim 38 wherein the thin film transistors is formed on the lower substrate and below the upper substrate.